

# United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.usplo.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/040,115	01/03/2002	Hideo Asano	ARC920010055US1 8776 -		
33360	7590 12/10/2003		EXAMINER		
MARK D. M		FIGUEROA, NATALIA			
IBM ALMAD 650 HARRY	DEN RESEARCH CENT ROAD	ART-UNIT	PAPER NUMBER		
CHTA/J2B	2. 05.00	2651			
SAN JOSE, O	CA 95120		DATE MAILED: 12/10/200	3	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Applica	tion No.	Applicant(s)					
Office Action Occurrence	10/040,	115	ASANO ET AL.					
Office Action Summary	Examin	er	Art Unit					
		Figueroa	2651					
The MAILING DATE of this communication of the second se	nication appears on t	he cover sheet with the c	correspondence add	dress				
A SHORTENED STATUTORY PERIOD I THE MAILING DATE OF THIS COMMUN  - Extensions of time may be available under the provision after SIX (6) MONTHS from the mailing date of this com  - If the period for reply specified above is less than thirty ( - If NO period for reply is specified above, the maximum s - Failure to reply within the set or extended period for repl - Any reply received by the Office later than three months earned patent term adjustment. See 37 CFR 1.704(b).  Status	NICATION. us of 37 CFR 1.136(a). In no sumunication. (30) days, a reply within the statutory period will apply and by will, by statute, cause the a	event, however, may a reply be tin tatutory minimum of thirty (30) day will expire SIX (6) MONTHS from pplication to become ABANDONE	nely filed  s will be considered timely the mailing date of this co (35 U.S.C. § 133).	mmunication.				
1) Responsive to communication(s) file	led on .							
•	2b)⊠ This action is	non-final.						
3) Since this application is in condition closed in accordance with the prac				merits is				
Disposition of Claims								
4) Claim(s) 1-9 is/are pending in the a	application.							
4a) Of the above claim(s) is/	are withdrawn from o	consideration.						
5) Claim(s) is/are allowed.								
6)⊠ Claim(s) <u>1,2,4,5,7 and 9</u> is/are reje	cted.							
7)⊠ Claim(s) <u>3, 6 and 8</u> is/are objected	to.							
8) Claim(s) are subject to restri	iction and/or election	requirement.						
Application Papers								
9) The specification is objected to by the								
# · ·	10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.							
Applicant may not request that any obj		•	. ,					
Replacement drawing sheet(s) including								
11) The oath or declaration is objected	to by the Examiner.	Note the attached Office	Action or form PT	O-152.				
Priority under 35 U.S.C. §§ 119 and 120								
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> <li>13) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet.</li> <li>37 CFR 1.78.</li> </ul>								
	a) The translation of the foreign language provisional application has been received.							
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.								
Attachment(s)		_						
Notice of References Cited (PTO-892)	(DTO 0.40)	4) Interview Summary						
<ul> <li>2) ☐ Notice of Draftsperson's Patent Drawing Review (</li> <li>B) ☐ Information Disclosure Statement(s) (PTO-1449)</li> </ul>		5) Notice of Informal F 6) Other:	atent Application (PTO	-152)				
Retent and Trademack Office		·						

Art Unit: 2651

.5.

### **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

1. Claims 1, 5 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tetsuya (EPO 0481752 A1) in view of Takashi et al (USPN 6,519,715).

Regarding claim 1: Tetsuya discloses a method for encoding and decoding blocks having a predetermined number of sectors of data bytes to detect and correct data bytes in error in each sector of a block, the method comprising the step of:

(a) generating sector level check bytes for each sector in the block responsive to the data bytes in each sector according to a first level of an error correction code, and generating block level check bytes for at least one sector in the block responsive to the sector level check bytes of

Art Unit: 2651

at least two sectors, including the at least one sector, according to at least a second level of the error correction code (abstract and fig. 5 and disclosure thereof).

Tetsuya fails to explicitly teach the step of (b) processing the block to detect and correct data bytes in error in each sector within the capability of the sector level check bytes, to detect and correct data bytes in error in the at least two sectors that exceed the correction capability of the sector level check bytes but within the correction capability of the block level check bytes, or to indicate that the data bytes in error in the at least two sectors exceed the correction capability of each of the sector level check bytes and the block level check bytes. However, Takashi et al disclose such on (col. 13, lines 38-57).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the apparatus as disclosed by Tetsuya with the above teachings from Takashi et al to include capability means to correct errors, hence selecting the right capability to correct errors and maximizing data storage.

Regarding claim 5: Tetsuya further discloses that the blocks represent audio and visual information (col. 1. lines 24-25).

Regarding claim 9: Apparatus claim 9 is drawn to the apparatus corresponding to the method of using same as claimed in claim 1. Therefore apparatus claim 9 corresponds to method claim 1, and is rejected for the same reasons of obviousness as used above.

2. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Tetsuya and Takashi et al as applied to claim 1 above, and further in view of Lee (USPN 6,405,342).

Regarding claim 2: Tetsuya and Takashi et al are relied upon for the same reasons as stated

Art Unit: 2651

in the above rejections. Tetsuya and Takashi et al fail to explicitly teach the method further re-generating the block level check bytes for the at least one sector responsive to the data bytes in error detected in each sector. However, Lee discloses such on (col. 14, lines 46-63).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the apparatus as disclosed by Tetsuya and Takashi et al with the above teachings from Lee to include the regeneration of bytes, hence correcting errors and maximizing the data storage.

3. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Tetsuya and Takashi et al as applied to claim 1 above, and further in view of Ofer et al (USPN 5,719,885).

Regarding claim 4: Tetsuya and Takashi et al fail to explicitly teach that each sector has 512 data bytes and each block has eight sectors. However, Ofer et al disclose such on (col. 2, lines 1-10). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the apparatus as disclosed by Tetsuya and Takashi et al with the above teachings from Ofer et al to choose data storage parameters that are common in the art, hence making the system compatible to other systems.

4. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Tetsuya and Takashi et al as applied to claim 1 above, and further in view of Hashimoto et al (USPN 5,864,440).

Regarding claim 7: Tetsuya and Takashi et al fail to explicitly teach the method further receiving logical block addresses (LBAs) from a host operating system for each write/read command, wherein the LBAs are translated into physical locations within blocks located on a

Art Unit: 2651

track of a moving storage medium of a data storage device; controlling the step of generating when writing data bytes responsive to the LBAs; and controlling the step of processing when reading data bytes responsive to the LBAs. However, Hashimoto et al disclose such on (col. 4, lines 32-42 and col. 5, lines 43-50).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the apparatus as disclosed by Tetsuya and Takashi et al with the above teachings from Hashimoto et al to include LBAs to control the reading and writing of byte, hence recognizing the sectors with errors and skipping them for better function of the system.

### Allowable Subject Matter

5. Claims 3, 6 and 9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 3: The prior art of record, and in particular Tetsuya (EPO 0481752 A1), fails to teach or suggest a method for encoding and decoding disabling the step of generating the block level check bytes when the write command is fragmented and is less than or equal to one multi-sector block of data bytes.

Regarding claim 6: Regarding claim 3: The prior art of record, and in particular Tetsuya (EPO 0481752 A1), fails to teach or suggest a method for encoding and decoding wherein the at least two sectors are adjacent to each other.

Page 6

### Conclusion

- 6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Sato et al (USPN 6,496,311): Recording/Reproducing of AV-data. Chung et al (USPN 6,239,931): Error correction capability. Yang (USPN 6,163,871): Error correction code encoding.
- 7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Natalia Figueroa whose telephone number is (703) 305-1260. The examiner can normally be reached on Monday Thursday 8:30-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David R. Hudspeth can be reached on (703) 308-4825. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

NEM

DAVID HUDSPETH SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600